

L Number	Hits	Search Text	DB	Time stamp
1	2916	select\$3 near10 (DRAM or bank or array) near10 activate\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:24
3	0	(select\$3 near10 (DRAM or bank or array) near10 activate\$1 with complementary) same advantage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:25
4	0	(select\$3 near10 (DRAM or bank or array) near10 activate\$1 with complementary) same advantage\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:25
5	0	(select\$3 near10 (DRAM or bank or array) near10 activate\$1 with complementary) same advanta\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:25
2	45	select\$3 near10 (DRAM or bank or array) near10 activate\$1 with complementary	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:34
6	106	activat\$3 near10 (logical adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:35
7	66	activat\$3 near10 (logical adj circuit) same (selectadj signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:38
8	1	activat\$3 near10 (logical adj circuit) same (selectadj signal) same complementary	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:37
9	0	activat\$3 near10 (logical adj circuit) same (select\$3 adj signal) same complementary	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:37
10	0	activat\$3 near10 (logical adj circuit) same select\$3 same complementary	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:38
11	1	activat\$3 near10 (logical adj circuit) same complementary	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:38
12	6	activat\$3 near10 (logical adj circuit) same (select\$3 adj signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:40
13	1	activat\$3 near10 (logical adj circuit) same complementary	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:41
14	2481	activat\$3 near10 complementary	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:41

15	195	(logical adj circuit) same complementary	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:41
16	37	(logical adj circuit) same complementary same select\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:54
17	1094	complementary same (select\$3 with activat\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:48
18	7	complementary same (MUX with select\$3 with activat\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:48
21	95337	select\$3 adj signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:54
22	413	(select\$3 adj signal) same activat\$3 same complementary	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:55
23	135	(select\$3 adj signal) with activat\$3 with complementary	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:56
24	0	((select\$3 adj signal) with activat\$3 with complementary) same advantage\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:56
25	3	((select\$3 adj signal) with activat\$3 with complementary) same (advantage\$1 or reason\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:57
26	104	((select\$3 adj signal) with activat\$3 with complementary) and advantage\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/05 15:57
-	608	(711/105).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/04 12:31
-	5697	(RAS or CAS or CS or WE) near5 address near5 data	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/04 12:32
-	103	((711/105).CCLS.) and ((RAS or CAS or CS or WE) near5 address near5 data)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/04 12:32
-	469	((RAS or CAS or CS or WE) near5 address near5 data) same logic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/04 12:48

-	48	((RAS or CAS or CS or WE) near5 address near5 data) same (logic adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 13:09
-	37	((RAS or CAS or CS or WE) near5 address near5 data) same (logic adj circuit)) and @ad<=20000929	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 12:48
-	73	((RAS or CAS or CS or WE) near5 address near5 data) same logic same DRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 12:48
-	53	((RAS or CAS or CS or WE) near5 address near5 data) same logic same DRAM) and @ad<=20000929	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 14:01
-	2324	(DRAM) same (logic adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 13:10
-	160	(DRAM) same (logic adj circuit) same (interface or (terminal adj group))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 13:11
-	34	(DRAM) same (logic adj circuit) same (interface or (terminal adj group)) same (control adj signal\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 13:59
-	217	internal\$3 near10 logic near10 DRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 13:58
-	46	(internal\$3 near10 logic near10 DRAM) and (interface or (terminal adj group)) same (control adj signal\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 13:59
-	28	((internal\$3 near10 logic near10 DRAM) and (interface or (terminal adj group)) same (control adj signal\$1)) and @ad<=20000929	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:25
-	387	(711/104).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:25
-	0	(710/129).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:25
-	0	(710/130).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:25
-	2892	(365/189.01).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:25

-	621	(365/189.08).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:26
-	1841	(365/230.01).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:26
-	3061	(365/230.06).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:26
-	1408	(365/230.08).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:26
-	1025	(365/190).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:26
-	421	(365/239).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:27
-	11	((DRAM) same (logic adj circuit)) and (711/104).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:27
-	0	((DRAM) same (logic adj circuit)) and (710/129).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:27
-	0	((DRAM) same (logic adj circuit)) and (710/130).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:27
-	50	((DRAM) same (logic adj circuit)) and (365/189.01).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:27
-	21	((DRAM) same (logic adj circuit)) and (365/189.08).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:27
-	33	((DRAM) same (logic adj circuit)) and (365/230.01).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:27
-	107	((DRAM) same (logic adj circuit)) and (365/230.06).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:27
-	29	((DRAM) same (logic adj circuit)) and (365/230.08).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/04 16:27

-	12	((DRAM) same (logic adj circuit)) and ((365/190).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/04 16:28
-	4	((DRAM) same (logic adj circuit)) and ((365/239).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/09/04 16:28